



Website: <http://www.allimagetool.com>

ABOUT THE INSTITUTE:

EAST WEST GROUP OF INSTITUTIONS, a well known and a prominent educational institutions crowns an absolute place among the top colleges in Bengaluru. The institutions was founded by a philanthropist and a social worker Shri. C M NAGARAJ in 1968, seizing the limelight as a vision for its reputation in the realm of technological education. EWGI upholds its unflinching recognition in all respects of academics and is celebrating 50th year of Academic Excellence, honing the life of every student with a code of conduct. EWGI offers vivid programs such as Engineering, Architecture, Science & Commerce, Management, Pharmacy, Polytechnic, Nursing, etc. East West Institute of Technology established in 2001 with the prime moto to serve the best quality based on technical education and the campus is advantageously located in a well-known green city of Bengaluru and has an extensive vicinity of 20 acres of land surrounded with congeal and pleasant learning atmosphere

ABOUT THE BITES:

BITES is an autonomous body promoted by Government in association with Institutions and Industries in Karnataka in the year 2000. GoK has setup BITES with the intent of maintaining high standards and quality in IT education. The main objectives of BITES are to enhance the quality of IT Education and improve the employability of Engineering Graduates. The Vision of BITES is "To establish Karnataka as the acknowledged leader in Information Technology by fostering high quality industry-relevant IT education". The key activity areas of BITES are: Advice on Policy, Challenge/ Competitions and Awards, Curriculum Development, Databases, Industry-Institute Interaction, Institutional Development, IT Education Standards and Leadership Development. BITES promotes, encourages, and deep dives into industry-academia equations on a number of platforms. It is this equation which will differentiate Winners from Losers. BITES is the way forward and partnering with it is imperative for every teaching institute, benefiting both the faculty and the student community.

ABOUT THE DEPARTMENT:

Electronics and Communication Engineering provides the perfect middle ground to equip with all the necessary skills to attain wide variety of career options. Electronics and communication has a foot in every door which gives broad domain knowledge of electronics, functional understanding of coding and how to utilize it to produce the desired results. The department has been training the students in the field of electronics since 2001. Millions of students are contributing throughout the world and their contribution can be seen at national level in many public sectors, research organization and defense sectors like DRDO, ISRO etc & most of the fortune 500 companies. The college has equally supported by providing state of the art laboratories were designed and applied for the student community, the hands on experience in VLSI, SOC, IMAGE PROCESSING, EMBEDDED SYSTEM and allied areas of signal processing. Students are enriched with this technological knowledge and expertise in identified domains. For the last five decades this branch has occupied and contributed remarkably and this department equally to be a part of nation building and creating ambience to the new horizon.

OBJECTIVES OF FDP:

1. The Main objective of this workshop is to make the students conceptually and practically aware about PCB design.
2. To enable faculties to upgrade to the changing curriculum and to increase employability opportunity for students.
3. This workshop focuses on needs for PCB design and steps involved in PCB design along with schematic design till gerbe verification.
4. This Program is designed to make students capable to design their own projects PCB up to industrial grade

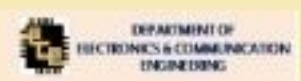
RESOURCE PERSON:

Sri. S M Narasimhan CEO,
Saroja Enterprises, Mysuru

The Resource Person Prof. S M Narasimhan has 30+ years of experience in Industry, Research, teaching, enterprising activities and project management. He completed B.E Electrical from National Institute of Engineering in 1984 and M.Tech in Electrical Engineering IIT, Mumbai in 1986. His experience includes research in commercial, industrial as well as military projects, design, development, manufacturing, testing conformal verification and field support. Some of the industries he has worked with are ITI, Dooravani Nagar Bangalore, ST Electronics Singapore, Saragur systems Singapore, Orthodyne Electronics, USA. His research interests are in the areas of embedded systems, IOT and motivation young minds with enterprising activities.

VENUE

DSP LAB, 1st Floor
Electronics & Communication Engineering
Date: 23rd & 24th May 2022
Time: 9.00 am to 4.30 pm



A Two Days Hands - On Workshop

Embedded System Board Design

23rd to 24th May 2022

Organised by

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

East West Institute of Technology

IN COLLABORATION WITH THE



BOARD FOR IT EDUCATION STANDARDS



#63, Off Magadi Road,
Bengaluru, Karnataka-560091
Ph.No.080 2328 8899
Website: <http://newit.ewgi.in>

Chief Patrons

Smt. Rashmi Ravi Kiran
Chairperson, East West Group of Institutions

Mr. Tejas Kiran
Secretary, East West Group of Institutions

Dr. K N Balasubramanya Murthy
Vice-Chancellor, Dayananda Sagar University & Chairman, BITES

Dr. T N Nagabhushana
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Director, East West Group of Institutions

Chief Coordinator & Convener

Dr. S G Hiremath
Prof & HOD, Dept . of ECE, EWIT

Faculty Co-coordinators
Prof. Manjula B B
Assoc. Professor, Mobile :9986680002

Prof. Divya B N
Asst. Professor, Mobile: 9945522770

Prof. Arpitha B V
Asst. Professor, Mobile No:8746094304

Prof. Babitha S Ullal
Asst. Professor, Mobile No: 9845300151

Participants :

Faculty, UG/ PG Students, Research Scholars.

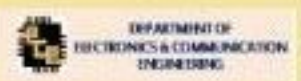
DAY 1 : Schematics Design, Library editing, net list verifications & ERC

Introduction to Library Editing, Schematic Library Editor, PCB Library Editor, Component attributes, Components creation, Introduction to Title pages, Modifying and Updating Components, Schematic Capture, Placing and wiring, Multiple Sheet Design, Assigning designators, Printing schematics, Electrical rules check and verification, Generation of netlist, checking netlist.

Days 2: PCB Design, routing, DRC and Gerber plots verifications

Netlist import and components placement, The PCB Editor workspace, Creating a new PCB board outline, Design rules and verification, Auto, Interactive and Manual Routing, Copper pouring, copper balancing, rooms, masking, Different layers verifications, Print PCB layers, Printing PCB layers and verification, Output files Generation (Gerber & NC), Gerber verification, PCB Finish guide lines etc.

No Registration Fee



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Name:.....

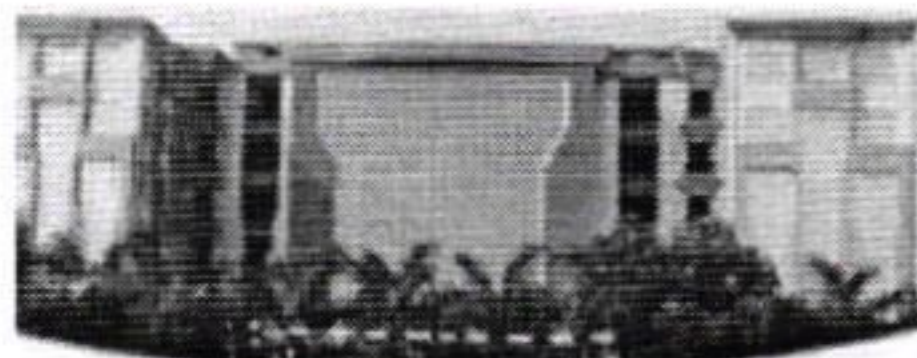
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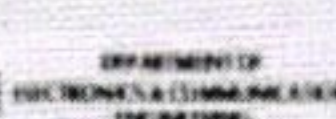
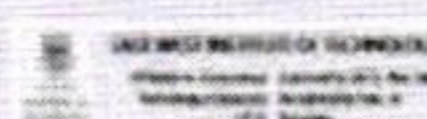
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Prof & HOD, Dept . of ECE, EWIT

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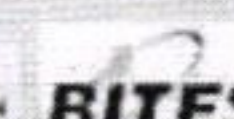
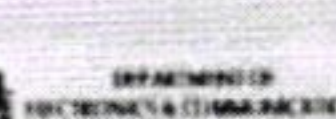
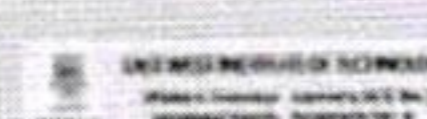
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Faculty, UG/ PG Students, Research Scholars.

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No Registration Fee



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23rd to 24th May 2022

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Department:

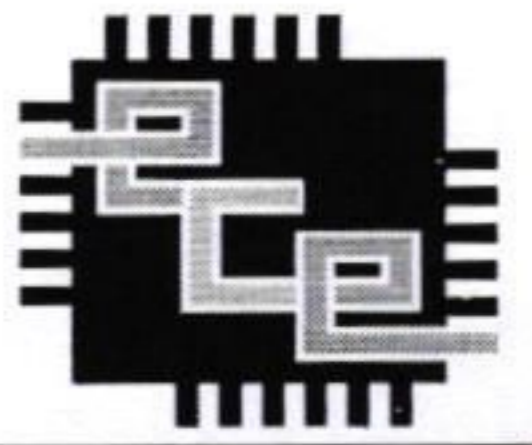
Organization:.....

Address:.....

Mobile No :

E-mail :

Bhaskar
Principal & Director
East West Institute of Technology
Bengaluru - 560 091



Timetable for Workshop/Seminar

Titlle: : EMBEDDED SYSTEM BOARD DESIGN

Date	Morning Session	LUNCH BREAK	After Session
	9:30 am to 1:00 pm		1:45 pm to 4:00 pm
23-05-2022	Introduction to Board Design, its importance and scope, Work place and Document editing, component creation guidelines, components library basics and Schematics, Schematics design entry and net list verification, PCB design, routing, DRC and final PCB finish guidelines, PCB plots and Gerber files verification and back annotation, EMI, ESD, UL, FCC conformance testing of the final PCB Design.		Introduction to Library Editing, Schematic Library Editor, PCB Library Editor Component attributes, Components creation, Introduction to Title pages, Modifying and Updating Components, Schematic Capture, Placing and wiring Multiple Sheet Design, Assigning designators, Electrical rules check and verification, Generation of netlist, checking netlist
24-05-2022	Netlist import and components placement, The PCB Editor workspace, Creating a new PCB board outline, Design rules and verification Auto, Interactive and Manual Routing, Copper pouring, copper balancing, rooms, masking		Different layers verifications, Print PCB layers , Printing PCB layers and verification, Output files Generation (Gerber & NC), Gerber verification, PCB Finish guide lines etc

& Co


 Principal & Director
 East West Institute of Technology
 Bengaluru - 560 091

Date: 23/05/22

Registration Form

Name : Jharun M

USN : IEW19EC116

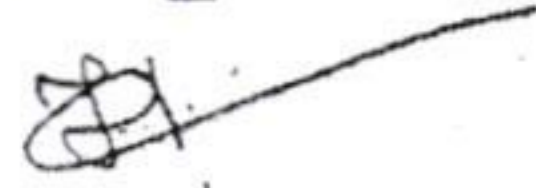
Department : ECE

Organization : East West Institute of Technology

Address : Anjananagar Campus

Mobile No : 7406104772

E-mail : tarun0124@yahoo.com

Participant Signature: 



EAST WEST

Date: 23/05/2022

Registration Form

Name : S. Chandan Kumar

USN : IEW19EC092

Department : ECE

Organization : EAST WEST INSTITUTE OF TECHNOLOGY

Address : Anjananagar Campus

Mobile No : 6304197469

Ee


Principal
East West Institute of Technology
Off Magadi Main Road, Bengaluru-91

Date: / /

Registration Form

Name : Vinod BK
USN : 1EW19EC124
Department : Electronics and communication
Organization : EWIT
Address : Anjananagar Bangalore
.....
.....

Mobile No : 831051142
vinodkumar171804@gmail.com

Vinod BK



Date: / /

Registration Form

Name : Kiran Kumar K.S.
USN : 1EW19EC05A
Department : ECE
Organization : EWIT
Address : EWIT Bangalore
.....
.....

Mobile No : 9880162936
E-mail : kiran.kumar.apr18@gmail.com
Participant Signature: Kiran

Ke

Bhaaskar
Principal
East West Institute of Technology
Off Magadi Main Road, Bengaluru-91



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Topic: A Two Days Hands - On Workshop "Embedded System Board Design"

Sl. NO	Students Name	USN	Contact No.	Day-1 23-05-2022		Day-2 24-05-2022	
				Morning Session	Afternoon Session	Morning Session	Afternoon Session
1.	MADHURA.R.N.	1EW19EC056	6362738664	Madhura	Madhura	Madhura	Madhura
2	Dhyanashree.R	1EW19EC032	7619182486	Dhyanashree	Dhyanashree	Dhyanashree	Dhyanashree
3.	Arunachal.C.R.	1EW19EC006	7349387691	Arunachal	Arunachal	Arunachal	Arunachal
4	Manu.P	1EW19EC060	8971177431	Manu	Manu	Manu	Manu
5	S.Chandru.Kumar	1EW19EC099	6304197469	S.Chandru	S.Chandru	S.Chandru	S.Chandru
6.	Kiron Kumar K.S.	1EW19EC052	9880162936	Kiron	Kiron	Kiron	Kiron
7.	Vinod.BK	1EW19EC124	8310511142	Vinod	Vinod	Vinod	Vinod
8.	Viveeth Kumar.P.G.	1EW19EC123	9113221841	Viveeth	Viveeth	Viveeth	Viveeth
9.	Chethan.P	1EW19EC023	9731511681	Chethan	Chethan	Chethan	Chethan

85

(Signature)
 Principal
 East West Institute of Technology
 Off Magadi Main Road, Bengaluru-91

SL. NO	Students Name	USN	Contact No.	Day-1 23-05-2022		Day-2 24-05-2022	
				Morning Session	Afternoon Session	Morning Session	Afternoon Session
10.	Sudeep M.P.	1EW19EC109	948048031	Sudeep M.P.	Sudeep M.P.	Sudeep M.P.	Sudeep M.P.
11.	Deepak K.M	1EW19EC028	9036849791	Deepak K.M	Deepak K.M	Deepak K.M	Deepak K.M
12.	Rakshith.GN	1EW19EC082	7348995085	Rakshith	Rakshith	Rakshith	Rakshith
13.	Jharum.M	1EW19EC116	7A06104772	Jharum	Jharum	Jharum	Jharum
14.	DARSHAN.S.HANALI	1EW19EC026	8073002847	D	D	D	D
15.	Bhramanth C.C	1EW19EC112	9008867163	Bhramanth	Bhramanth	Bhramanth	Bhramanth
16.	Gubhas S	1EW19EC110	7019140725	Gubhas	Gubhas	Gubhas	Gubhas
17.	Bukanya. M	1EW19EC111	9632033845	Bukanya	Bukanya	Bukanya	Bukanya
18.	Janusheer.G	1EW19EC115	9686923880	Janusheer	Janusheer	Janusheer	Janusheer
19.	Rakesh DN	1EW19EC080	6364048488	Rakesh	Rakesh	Rakesh	Rakesh
20.	Raghavem dsa.bnk	1EW19EC079	9380757095	Raghavem	Raghavem	Raghavem	Raghavem
21.	Rohan.S	1EW19EC089	9740483899	Rohan	Rohan	Rohan	Rohan

85

R. Prasad
Principal
East West Institute of Technology
Off Magadi Main Road, Bengaluru-91

SL. NO	Students Name	USN	Contact No.	Day-1 23-05-2022		Day-2 24-05-2022	
				Morning Session	Aternoon Session	Morning Session	Aternoon Session
22	Romanagonda.G.S	IEW19EC085	8073509313	[Signature]	[Signature]	[Signature]	[Signature]
23	Sadith. B	IEW19EC093	9980588007	[Signature]	[Signature]	[Signature]	[Signature]
24	Sevanth.G.S	IEW19EC098	6364784754	[Signature]	[Signature]	[Signature]	[Signature]
25	SHARATH.K	IEW19EC100	9972702540	[Signature]	[Signature]	[Signature]	[Signature]
26	Sudeep.K	IEW19EC108	9353253646	[Signature]	[Signature]	[Signature]	[Signature]
27	Nagendra Prasad.P.N	IEW19EC066	8762229800	[Signature]	[Signature]	[Signature]	[Signature]
28	Sai Sandhya.K	IEW19EC097	9066207201	[Signature]	[Signature]	[Signature]	[Signature]
29	Nisarga.H.S	IEW19EC068	9900798294	[Signature]	[Signature]	[Signature]	[Signature]
30	Vidyashree.H	IEW19EC122	7899152259	[Signature]	[Signature]	[Signature]	[Signature]
31	Tulasi.V	IEW19EC118	9019995698	[Signature]	[Signature]	[Signature]	[Signature]
32	Ranjitha G.S	IEW19EC088	8088275658	[Signature]	[Signature]	[Signature]	[Signature]
33	Pooja.H.N	IEW19EC073	7996855232	[Signature]	[Signature]	[Signature]	[Signature]

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[Signature]
Principal
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SL. NO	Students Name	USN	Contact No.	Day-1 23-05-2022		Day-2 24-05-2022	
				Morning Session	Afternoon Session	Morning Session	Afternoon Session
34.	Aishwarya . SN	1EW19EC0082	8431622644	Aishwarya	Aishwarya	Aishwarya	Aishwarya
35.	J. Cyathia Samuel	1EW19EC044	8453412708	Cyathia	Cyathia	Cyathia	Cyathia
36.	Kavya . S	1EW20FCH02	8105408927	Kavya . S	Kavya . S	Kavya . S	Kavya . S
37.	Raniya Sri . S	1EW19EC087	7760188533	272075	272075	272075	272075
38.	Sobhbhagya . S	1EW19EC007	7975439121	Sobhbhagya	Sobhbhagya	Sobhbhagya	Sobhbhagya
39.	Namasa . R	1EW20FC405	9611081021	Dhanu . R	Dhanu . R	Dhanu . R	Dhanu . R
40.	Vijitha . K	1EW19EC126	8553444555	Vijitha . K	Vijitha . K	Vijitha . K	Vijitha . K
41.	Sahana . M . S	1EW19EC095	6364269540	Sahana	Sahana	Sahana	Sahana
42.	Swathi . S	1EW19EC114	8317389003	Swathi	Swathi	Swathi	Swathi
43.	Veena Vinodhini . M . N	1EW19EC121	9611263484	Veena	veena	veena	Veena
44.	Duej . Mandlue	1EW19EC113	8971949616	Duej	Duej	Duej	Duej

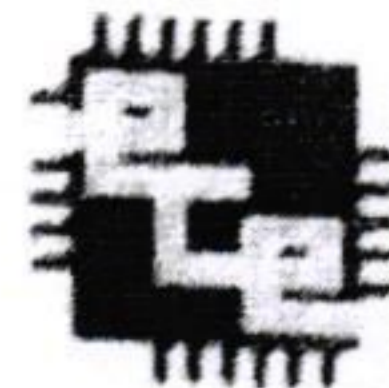
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Dhanu
Principal
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Off Magadi Main Road, Bengaluru - 93

EAST WEST INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

(Affiliated to VTU Belagavi, Approved by AICTE, New Delhi, Recognized by Govt. of Karnataka
Accredited by NAAC & Recognized U/S 2 (f) of the UGC, Act 1956)



Date: 24-05-2022

BRIEF REPORT ON

A Two Days Skill enrichment workshop on "**Embedded System Board Design**" in collaboration with the BITES

The workshop has been inaugurated by the Principal **Dr. K. Channakeshavalu**, HOD of ECE, **Dr. S G Hiremath**, Resource person **Mr. S M Narasimhan**, and HOD of other departments along with the faculties and students at 9.30 am on 23/05/2022. 44 students were participated in the workshop.

Mr. S M Narasimhan sir had given brief introduction about the **Embedded system Board Design**. He gave vital information about the software that is used to build PCB using PCAD2002 trial version through which students were able to create library, add library, schematic building and PCB creation.

The five steps followed to create PCB is:

symbol generation, Part Library, Path Generation, Netlist generation, Schematic representation, and upload the PCB to get it in Gerber format.

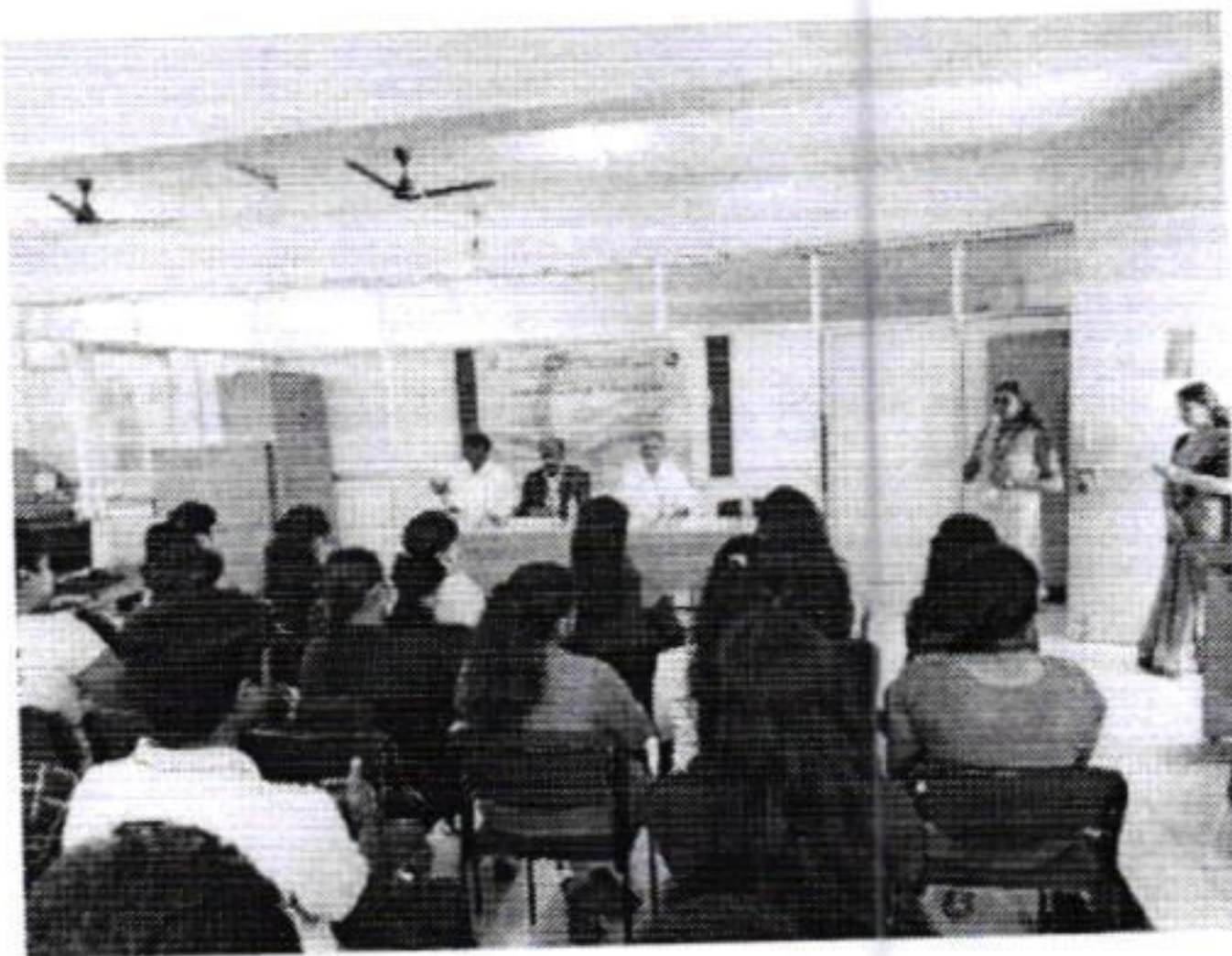
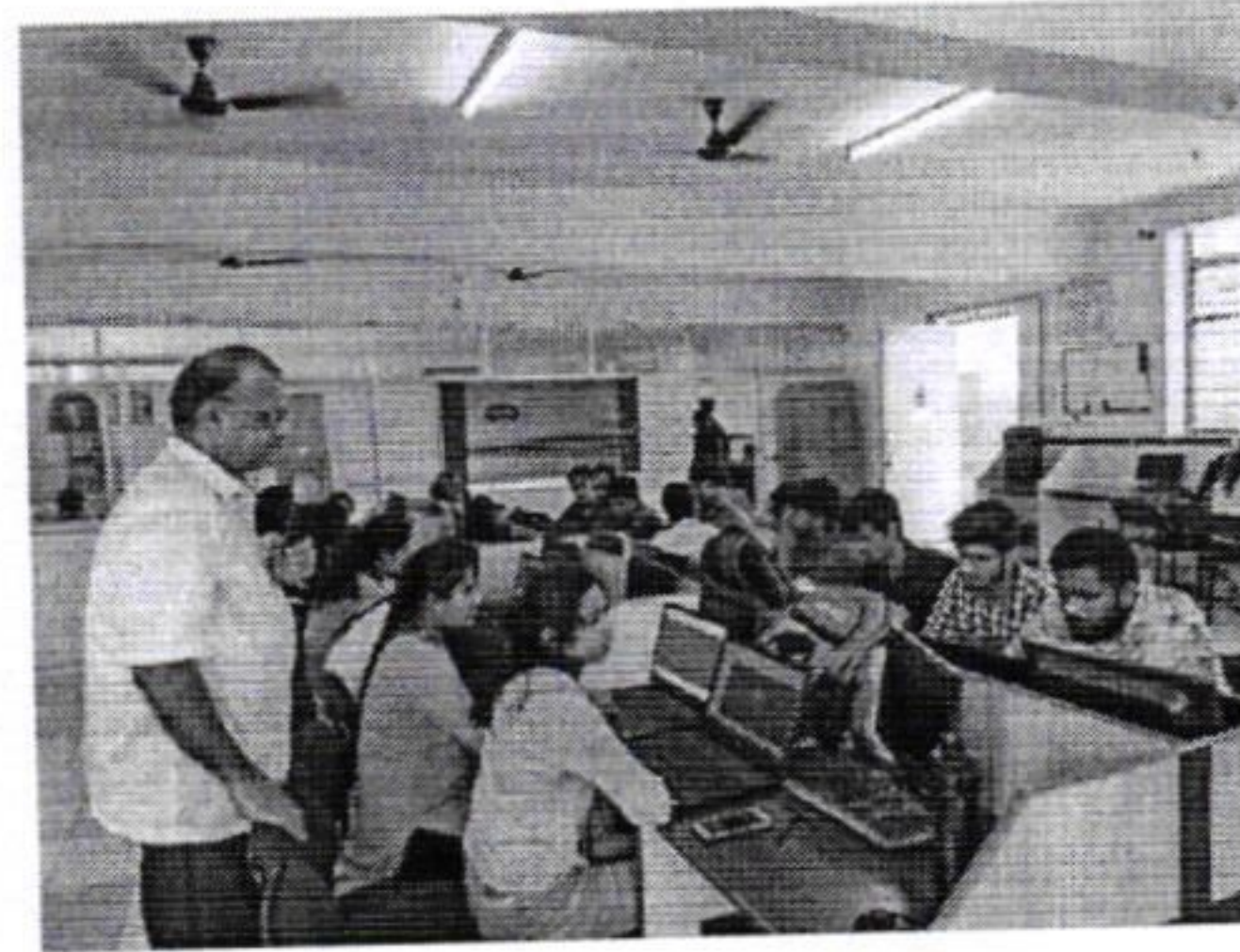
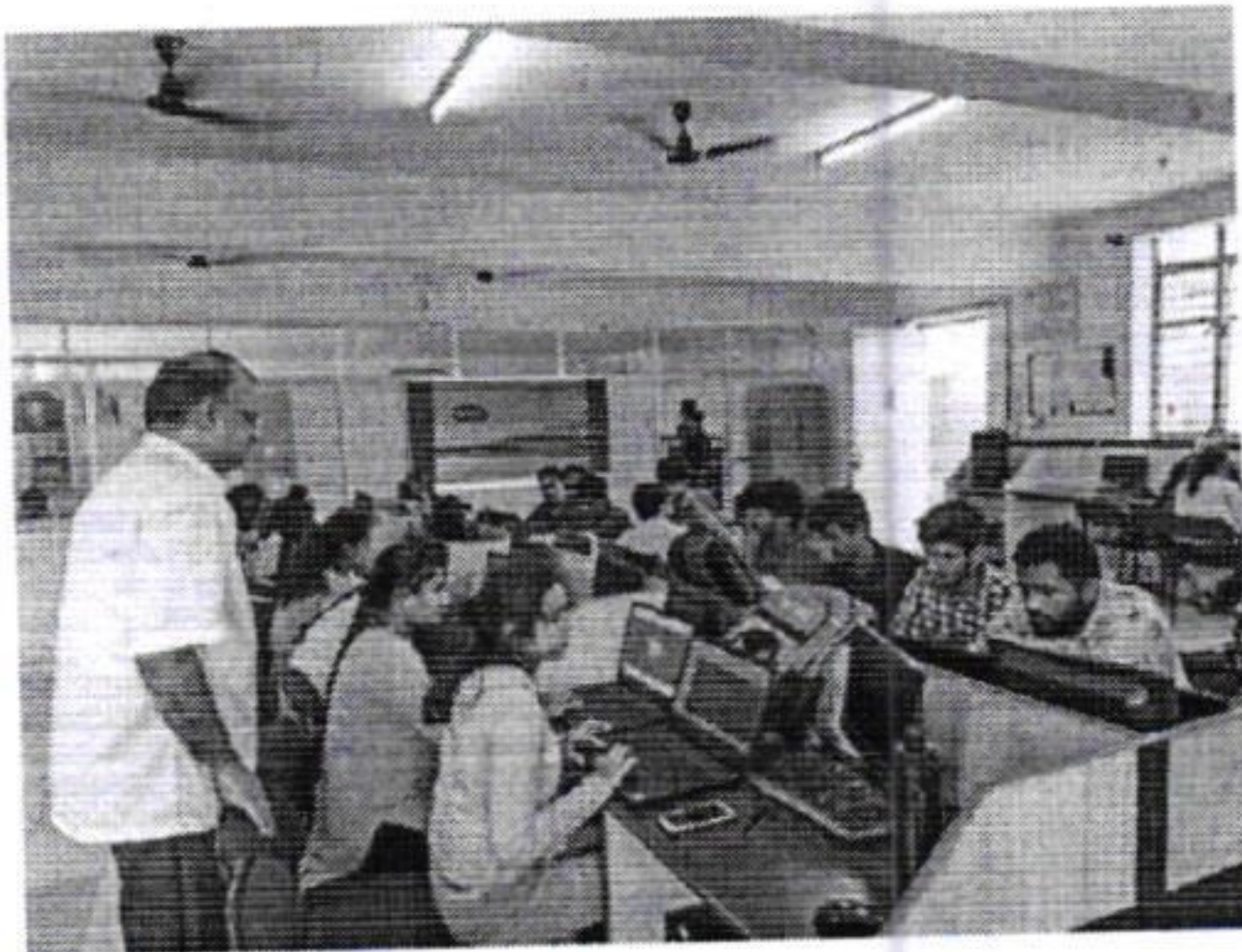
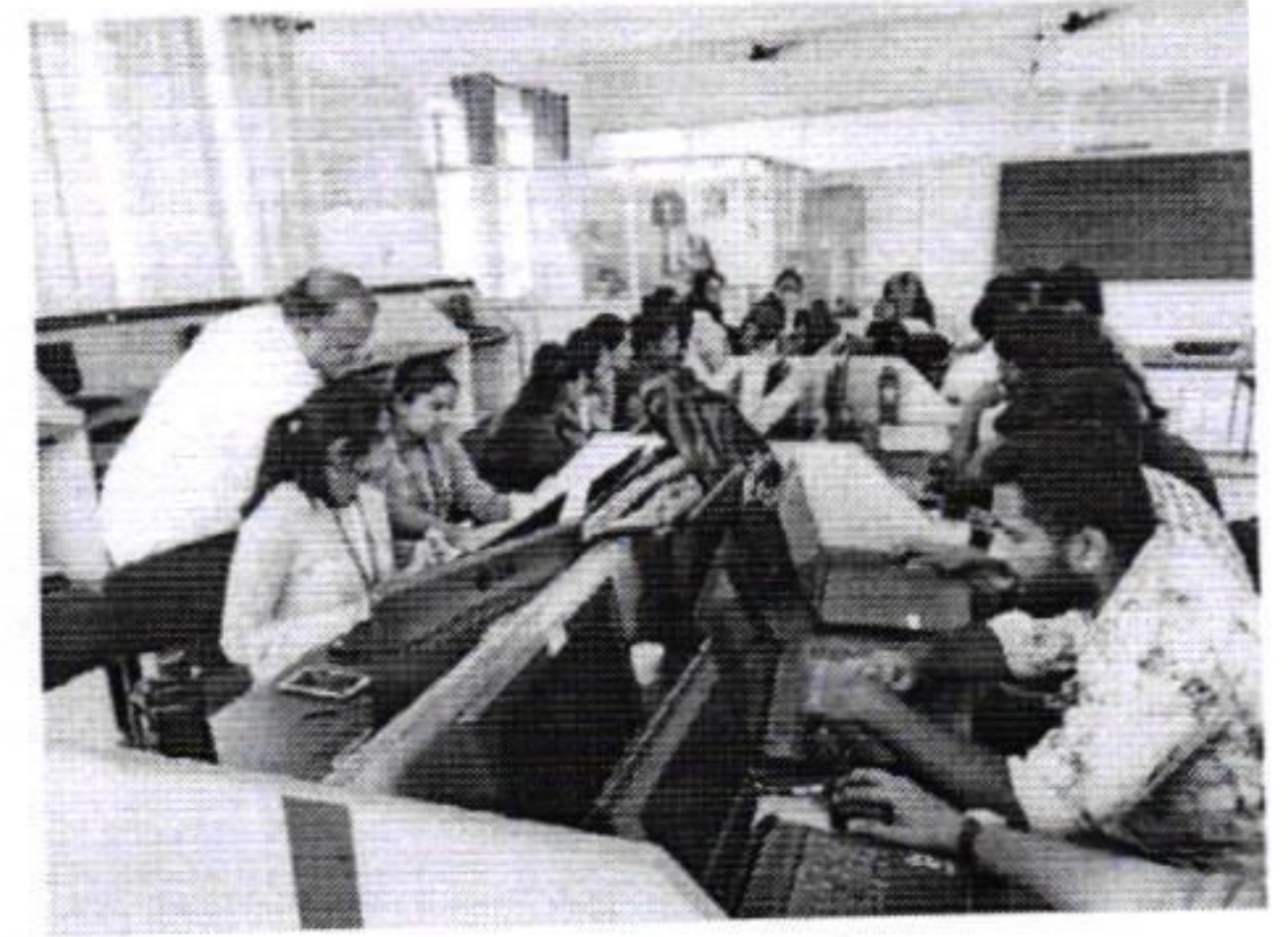
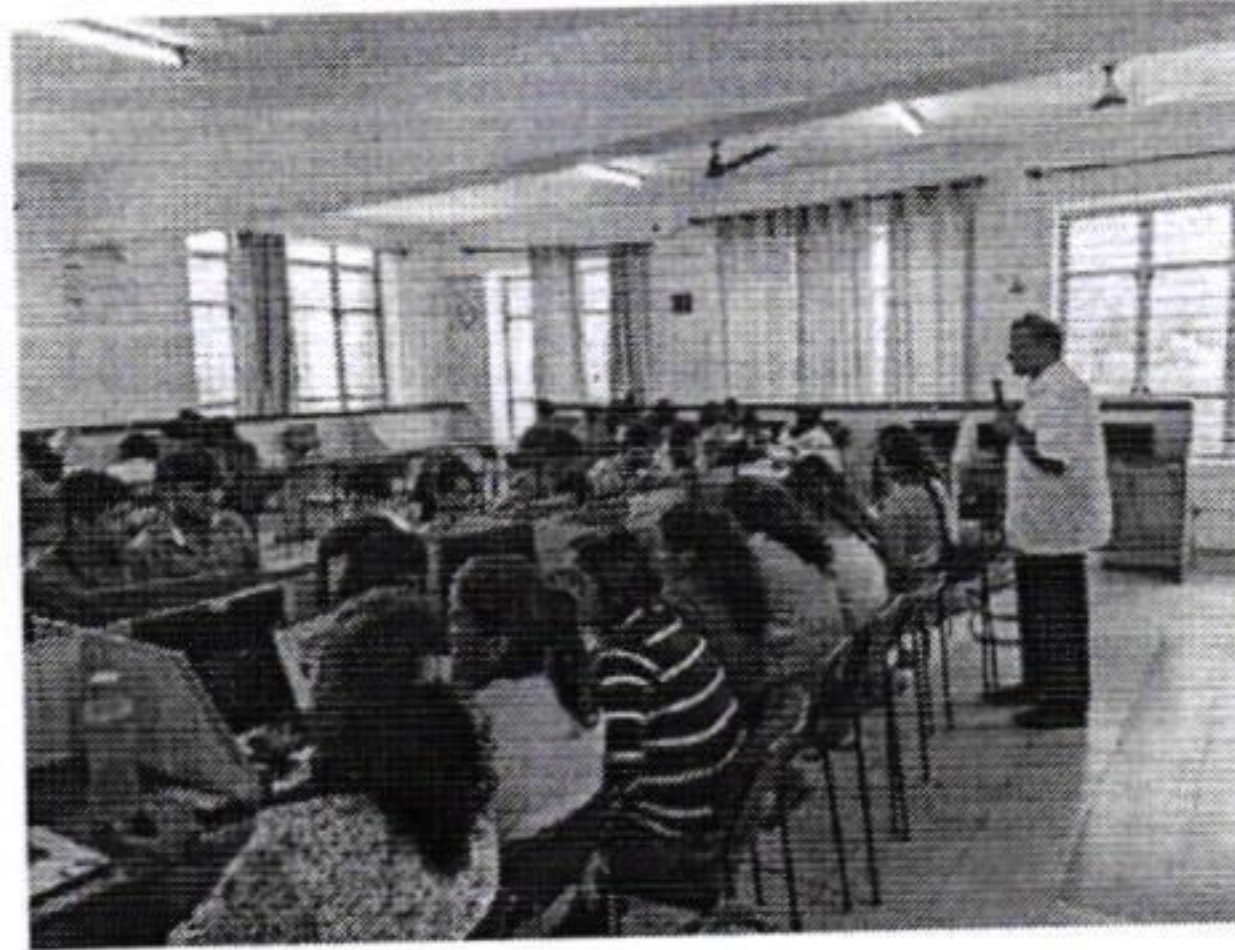
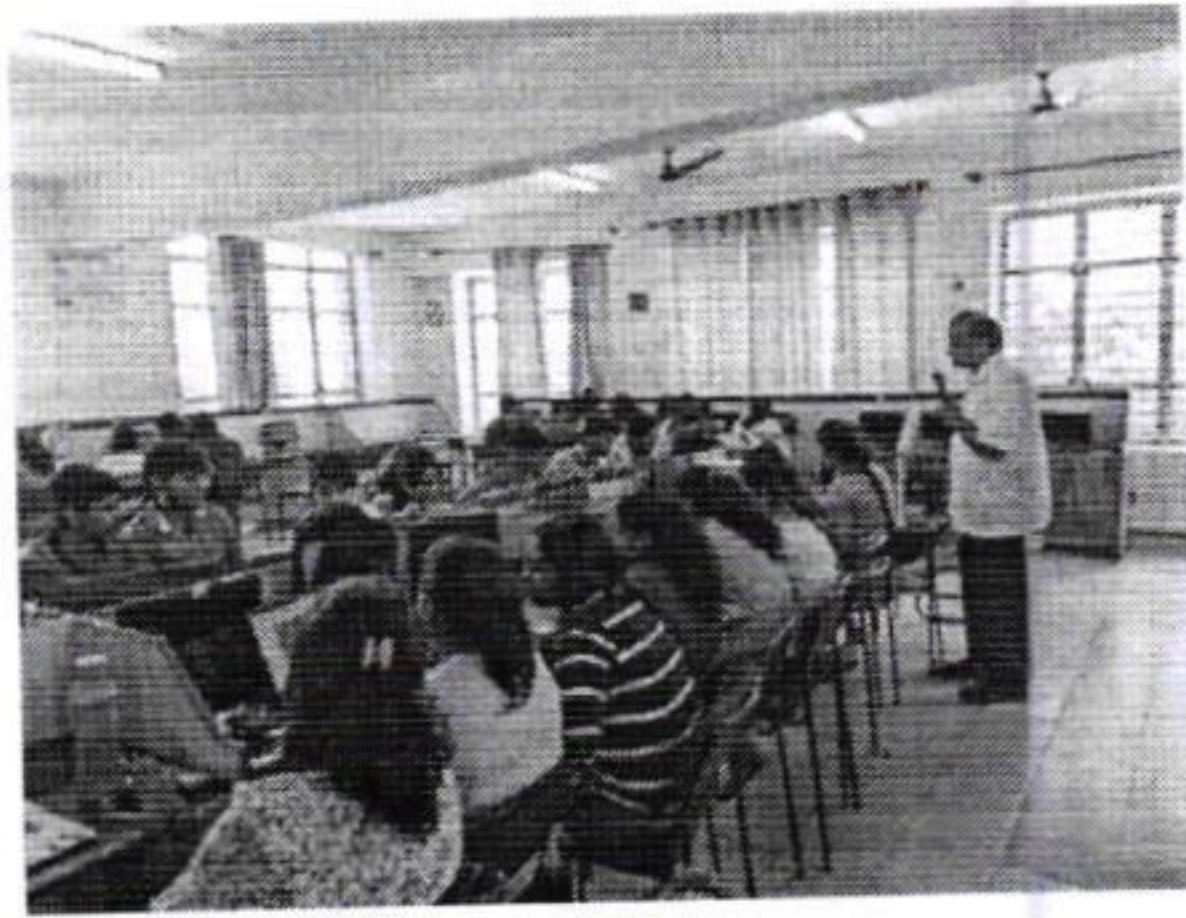
To create Schematic existing library was used. Students used different sheets and created a circuit in general for further implementations. Netlist is generated and checked for the errors. All the components were loaded in PCB using netlist. By creating a border layer memory usage can be reduced. Top layer to bottom layers are routed for the connections. Later print setup was done through which all the layers could be differentiated and could know what is present on which layer through specifications. Then exported the PCB to Gerber, where circuit could be viewed in Gerber format. Later using online Gerber view students exported the layers file and viewed the output. This method was used by students to create a sample PCB design in two days skill enrichment programme on **Embedded system Board Design**

Mr. S M Narasimhan sir is an excellent resource person with lot of patience and he started to solve all the problems faced by the students while experimenting. This workshop motivated students to take up this as a base for their project work. At the end of the workshop **Principal** and **HOD of ECE Department** inspired students with their valuable words and also handed over certificates to the students.

HOD, ECE

Dr. S G Hiremath

Seminar on A Two Days Skill enrichment workshop –on “Embedded System Board Design” on Conducted by Sri. S M Narasimhan CEO, Saroja Enterprises, Mysuru On 23-05-2022 & 24-05-2022



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